

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

Patent No.:

Filed:

Issue Date:

Title:

Commissioner for Patents
Washington, D.C. 20231

**POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(REVOCATION OF PRIOR POWERS)**

As assignee of record of each of the patents listed in the table of attachment A,

REVOCATION OF PRIOR POWERS OF ATTORNEY

all powers of attorney previously given in each of the listed patents are hereby revoked, and

NEW POWER OF ATTORNEY

the following attorneys/agents are hereby appointed to prosecute this patent/application and to transact all business in the Patent and Trademark Office connected therewith: I hereby appoint all attorneys of Thomas, Kayden, Horstemeyer & Risley, LLP, who are listed under the USPTO Customer Number shown below as the attorneys to prosecute this patent/application and to transact all business in the United States Patent and Trademark Office connected therewith, recognizing that the specific attorneys listed under that Customer Number may be changed from time to time at the sole discretion of Thomas, Kayden, Horstemeyer & Risley, LLP, and request that all correspondence about the application be addressed to the address filed under the same USPTO Customer Number:

15650

Patent Trademark Office

Please direct all future correspondence and telephone calls to:

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ASSIGNEE OF ENTIRE INTEREST

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Irvine, California 92617-3038

ASSIGNEE CERTIFICATION

The certification under 37 C.F.R. §3.73(b) establishing the right of assignee to take action is attached hereto along with documentation evidencing same. Further, in my official position with Broadcom Corporation, I am authorized to sign documents and otherwise act on its behalf in connection with the management and handling of patent applications and other intellectual property matters.

Date: _____

9/12/11



Dee Henderson

Director - Intellectual Property Administration
Broadcom Corporation

ATTACHMENT A

No.	Patent No.	BD No.	Patent Title	Issue Date	Assignment to BD (Reel/Frame)
1	6,771,321	BU2010	METHOD AND SYSTEM FOR PERFORMING PAL LUMA TWO LINE VERTICAL COMBING	8/3/2004	<u>012355/0746</u>
2	7,426,676	BU3406	DATA RETRIEVAL FROM A STORAGE DEVICE USING A COMBINED ERROR CORRECTION AND DETECTION APPROACH	9/16/2008	<u>014617/0758</u>
3	7,451,362	BU3408	METHOD AND SYSTEM FOR ONBOARD BIT ERROR RATE (BER) ESTIMATION IN A PORT BYPASS CONTROLLER	11/11/2008	<u>014703/0674</u>
4	6,882,345	BU1574	METHOD AND SYSTEM FOR EFFICIENTLY LOADING PRIMITIVES INTO PROCESSORS OF A GRAPHICS SYSTEM	4/19/2005	<u>012276/0527</u>
5	6,389,092	BU1651	STABLE PHASE LOCKED LOOP HAVING SEPARATED POLE	5/14/2002	<u>013108/0284</u>
6	6,549,599	BU1651D1			
7	7,995,687	BU5558	FAST AND RELIABLE CHANNEL CLASSIFICATION ALGORITHMS IN BLUETOOTH NETWORKS TO DETECT AND AVOID 2.4 GHZ INTERFERERS	8/9/2011	<u>019108/0436</u>
8	7,869,541	BU5626	METHOD AND SYSTEM FOR DIRECT AND POLAR MODULATION USING A TWO INPUT PLL	1/11/2011	<u>018741/0713</u>

No.	Patent No.	BD No.	Patent Title	Issue Date	Assignment to BD (Reel/Frame)
9	7,987,095	BU5630	METHOD AND SYSTEM FOR DUAL MODE SUBBAND ACOUSTIC ECHO CANCELLER WITH INTEGRATED NOISE SUPPRESSION	7/26/2011	<u>018867/0950</u>
10	7,610,439	BU5675	METHOD AND SYSTEM FOR HARDWARE IMPLEMENTATION OF RESETTING AN EXTERNAL TWO-WIRED EEPROM	10/27/2009	<u>018993/0536</u>
11	7,908,428	BU5675C1			
12	7,869,781	BU5900	METHOD AND SYSTEM FOR MITIGATING THE EFFECTS OF PULLING IN MULTIPLE PHASE LOCKED LOOPS IN MULTI-STANDARD SYSTEMS	1/11/2011	<u>018764/0115</u>
13	7,492,223	BU5914	METHOD AND SYSTEM FOR A HIGHLY EFFICIENT POWER AMPLIFIER UTILIZING DYNAMIC BIASING AND PREDISTORTION	2/17/2009	<u>019559/0667</u>
14	7,679,449	BU5914C1			
15	7,184,058	BU1208C1			
16	7,746,354	BU1208C2			

No.	Patent No.	BD No.	Patent Title	Issue Date	Assignment to BD (Reel/Frame)
17	6,853,385	BU1233	VIDEO, AUDIO AND GRAPHICS DECODE, COMPOSITE AND DISPLAY SYSTEM	2/8/2005	<u>011581/0993</u>
18	7,468,992	BU1357.2C1			
19	7,835,407	BU1357.2C2			
20	7,130,579	BU1545.9	ADAPTIVE RADIO TRANSCEIVER WITH A WIDE TUNING RANGE VCO	10/31/2006	<u>011827/0032</u>
21	6,975,838	BU154512	ADAPTIVE RADIO TRANSCEIVER WITH NOISE SUPPRESSION	12/13/2005	<u>012144/0484</u>
22	7,136,622	BU154512C1			
23	6,807,585	BU20781	METHOD AND SYSTEM FOR HANDLING DATA PACKETS	10/19/2004	<u>022083/0433</u>
24	6,636,222	BU1233.1	VIDEO AND GRAPHICS SYSTEM WITH AN MPEG VIDEO DECODER FOR CONCURRENT MULTI-ROW DECODING	10/21/2003	<u>011581/0971</u>

No.	Patent No.	BD No.	Patent Title	Issue Date	Assignment to BD (Reel/Frame)
25	7,277,099	BU1233.1C1			
26	7,110,006	BU1233C1			
27	7,667,715	BU1233C2			
28	6,630,945	BU1203	GRAPHICS DISPLAY SYSTEM WITH GRAPHICS WINDOW CONTROL MECHANISM	10/7/2003	<u>010625/0771</u>
29	7,530,027	BU1203C1	GRAPHICS DISPLAY SYSTEM WITH GRAPHICS WINDOW CONTROL MECHANISM	5/5/2009	